



Hardware Design Verification: Simulation and Formal Method-Based Approaches

By William K. Lam

Pearson Education (US), United States, 2008. Paperback. Book Condition: New. 234 x 176 mm. Language: English . Brand New Book. The Practical, Start-to-Finish Guide to Modern Digital Design Verification As digital logic designs grow larger and more complex, functional verification has become the number one bottleneck in the design process. Reducing verification time is crucial to project success, yet many practicing engineers have had little formal training in verification, and little exposure to the newest solutions. Hardware Design Verification systematically presents today's most valuable simulation-based and formal verification techniques, helping test and design engineers choose the best approach for each project, quickly gain confidence in their designs, and move into fabrication far more rapidly. College students will find that coverage of verification principles and common industry practices will help them prepare for jobs as future verification engineers. Author William K. Lam, one of the world's leading experts in design verification, is a recent winner of the Chairman's Award for Innovation, Sun Microsystems most prestigious technical achievement award. Drawing on his wide-ranging experience, he introduces the foundational principles of verification, presents traditional techniques that have survived the test of time, and introduces emerging techniques for today's most...



READ ONLINE
[4.19 MB]

Reviews

This is actually the best book i actually have go through right up until now. It generally will not price an excessive amount of. I discovered this book from my dad and i suggested this book to understand.

-- **Norma Carroll**

The ebook is not difficult in study preferable to understand. it was writtern quite flawlessly and beneficial. You are going to like just how the author compose this book.

-- **Leola Smith**

Other Kindle Books



Index to the Classified Subject Catalogue of the Buffalo Library; The Whole System Being Adopted from the Classification and Subject Index of Mr. Melvil Dewey, with Some Modifications .

Rarebooksclub.com, United States, 2013. Paperback. Book Condition: New. 246 x 189 mm. Language: English . Brand New Book ***** Print on Demand *****.This historic book may have numerous typos and missing text. Purchasers can usually download a free scanned copy of the...



The Trouble with Trucks: First Reading Book for 3 to 5 Year Olds

Anness Publishing. Paperback. Book Condition: new. BRAND NEW, The Trouble with Trucks: First Reading Book for 3 to 5 Year Olds, Nicola Baxter, Geoff Ball, This is a super-size first reading book for 3-5 year olds, with an engaging story, colourful pictures...



Book Finds: How to Find, Buy, and Sell Used and Rare Books (Revised)

Perigee. PAPERBACK. Book Condition: New. 0399526544 Never Read-12+ year old Paperback book with dust jacket-may have light shelf or handling wear-has a price sticker or price written inside front or back cover-publishers mark-Good Copy- I ship FAST with FREE tracking!! * I...



Everything Ser The Everything Green Baby Book From Pregnancy to Babys First Year An Easy and Affordable Guide to Help Moms Care for Their Baby And for the Earth by Jenn Savedge 2009 Paperback

Book Condition: Brand New. Book Condition: Brand New.



Weebies Family Halloween Night English Language: English Language British Full Colour

Createspace, United States, 2014. Paperback. Book Condition: New. 229 x 152 mm. Language: English . Brand New Book ***** Print on Demand *****.Children s Weebies Family Halloween Night Book 20 starts to teach Pre-School and Junior Children how to read with this...



Games with Books : 28 of the Best Childrens Books and How to Use Them to Help Your Child Learn - From Preschool to Third Grade

Book Condition: Brand New. Book Condition: Brand New.

Out of print. Hardware Design Verification: Simulation and Formal Method-Based Approaches. William K. Lam. ©2005 | Pearson | Out of print. Engineers started out as designers and gradually transitioned to design verification. Unlike design techniques and methodologies, a broad range of verification knowledge is loosely organized and informally acquired through hands-on experiences. Keywords: hardware design; functional verification; simulation-based-verification; co-simulation; testbench automation; reaction checking; transaction level modeling (TLM). I. INTRODUCTION. Nowadays, hardware systems become more and more complex consuming lots of resources for their design and test. Two main approaches to functional verification of hardware designs are formal methods and simulation-based methods [3]. It is well known that formal methods are exhaustive (in a sense) but do not scale well and can be applied only at the later design stages, when the requirements are rather stable. Simulation-based methods are not exhaustive, but they are much more flexible and thereby employed at different stages. Hardware Design Verification book. Read reviews from world's largest community for readers. Serves as a guide to Modern Digital Design Verification. This... Goodreads helps you keep track of books you want to read. Start by marking "Hardware Design Verification: Simulation and Formal Method-Based Approaches" as Want to Read: Want to Read saving | Want to Read.